

AMENDMENTS TO THE CLAIMS:

Please amend the claims as follows, substituting any amended claim(s) for the corresponding pending claim(s):

1. (Currently Amended) A method of forming a linear photosensor array, comprising:

forming a plurality of integrated circuits each including

photosensors formed in a line along a length of the integrated circuit within a portion of the integrated circuit remaining exposed after packaging, and

a plurality of conductive leads adapted for soldering to a circuit board;

mounting a plurality of integrated circuit die on a lead frame strip with a separation between the mounted integrated circuit die approximately equal to a kerf width for a singulation saw to be used in separating the packaged integrated circuits;

mounting the integrated circuits with the portions containing the photosensors in alignment on a circuit board; and

soldering at least some of the leads for each integrated circuit to the circuit board.

2. (Currently Amended) The method of claim 1, comprising:

packaging each of the plurality of integrated circuit packages by:

affixing ~~an~~ the integrated circuit die to a ~~the~~ lead frame strip;

connecting the integrated circuit die to selected portions of the lead frame strip with bond wires; and

encapsulating a portion of the lead frame strip and the integrated circuit die except for the exposed region, wherein the exposed region of the integrated circuit die remains exposed to external ambient light.

3. (Currently Amended) The method of claim 2, comprising:

after affixing the integrated circuit die to the lead frame strip and connecting the bond wires, mounting the lead frame strip with the integrated circuit die and bond wires in a mold with a portion of the mold in contact with the exposed region of the integrated circuit die to prevent encapsulating material from adhering to the exposed region of the integrated circuit die.

4. (Currently Amended) The method of claim 3, further comprising:

mounting the lead frame strip with the integrated circuit die and bond wires in a mold having a sloped surface adjacent to the portion of the mold contacting the exposed region of the integrated circuit die, wherein the sloped surface forms one surface of a mold cavity receiving the bond wires when the lead frame strip with the integrated circuit die is mounted in the mold.

5. (Canceled)

6. (Currently Amended) ~~The method of claim 1, comprising:~~ A method of forming a linear photosensor array, comprising:

forming a plurality of integrated circuits each including

photosensors formed in a line along a length of the integrated circuit within a portion of the integrated circuit remaining exposed after packaging, and

a plurality of conductive leads adapted for soldering to a circuit board;

packaging the integrated circuits with the portion containing the photosensors exposed; and

mounting the integrated circuits with the portions containing the photosensors in alignment on a circuit board, including mounting adjacent packaged integrated circuits in contact with each other[.]; and

soldering at least some of the leads for each integrated circuit to the circuit board.

7. (Currently Amended) ~~The method of claim 1, further comprising:~~ A method of forming a linear photosensor array, comprising:

forming a plurality of integrated circuits each including

photosensors formed in a line along a length of the integrated circuit within a portion of the integrated circuit remaining exposed after packaging, and

a plurality of conductive leads adapted for soldering to a circuit board;

mounting the integrated circuits with the portions containing the photosensors in alignment on a circuit board; and

soldering at least some of the leads for each integrated circuit to the circuit board, including
soldering only leads on one side of each integrated circuit to the circuit board, leaving leads on an other side of the packaged integrated circuits in floating contact with conductive structures on the circuit board to facilitate adjustment and removal of integrated circuits.

8. (Currently Amended) An integrated circuit package for a linear photosensor array, comprising:

a lead frame including a die paddle and a plurality of leads;

an integrated circuit die affixed to the die paddle, the integrated circuit die including a plurality of photosensitive devices linearly aligned along a length of an upper surface of the integrated circuit die; and

packaging material encapsulating a portion of the lead frame and the integrated circuit die except for a region of the integrated circuit die containing the photosensitive devices, wherein the region containing the photosensitive devices remains exposed through the packaging material, and wherein the packaging material does not cover ends of the integrated circuit die.

9. (Previously Presented) The integrated circuit package of claim 8, wherein the die paddle of the lead frame is shorter in length than the integrated circuit die.

10. (Original) The integrated circuit package of claim 8, wherein the integrated circuit is affixed to the lead frame with an adhesive.

11. (Canceled)

12. (Previously Presented) The integrated circuit package of claim 8, further comprising:
bond wires connecting the integrated circuit die to selected portions of the lead frame,
wherein the packaging material encapsulates the bond wires.

13. (Currently Amended) A linear photosensor array, comprising:

a circuit board; and

a plurality of packaged integrated circuits mounted on the circuit board, wherein each packaged integrated circuit includes an array of photosensors linearly aligned along a length of a surface region of an integrated circuit die therein and a plurality of conductive leads adapted for soldering to the circuit board,

wherein the packaged integrated circuits are mounted in a line on the circuit board with the surface regions of each packaged integrated circuit in alignment, and

wherein adjacent packaged integrated circuits on the circuit board are mounted in contact with each other such that the array of photosensors on each integrated circuit die form a single, long, continuous photosensor array, and

wherein at least some of the leads for each packaged integrated circuit are soldered to the circuit board.

14. (Currently Amended) ~~The linear photosensor array of claim 13,~~ A linear photosensor array,
comprising:

a circuit board; and

a plurality of packaged integrated circuits mounted on the circuit board,

wherein each packaged integrated circuit includes an array of photosensors linearly aligned
along a length of a surface region of an integrated circuit die therein and a lead frame having a
plurality of conductive leads adapted for soldering to the circuit board,

wherein the packaged integrated circuits are mounted in a line on the circuit board with the
surface regions of each packaged integrated circuit in alignment,

wherein at least some of the leads for each packaged integrated circuit are soldered to the
circuit board,

wherein ~~the~~ each of the integrated circuit packages further comprises:

bond wires connecting the integrated circuit die to selected portions of the lead frame; and

~~packaging encapsulating~~ that encapsulates the bond wires, a portion of the lead frame, and
the integrated circuit die except for the surface region containing the array of photosensors and ends
of the integrated circuit die, ~~and the bond wires,~~ and

wherein the surface region of the integrated circuit die containing the array of photosensors
remains exposed to ambient light.

15. (Canceled)

16. (Currently Amended) ~~The linear photosensor array of claim 13,~~ A linear photosensor array,
comprising:

a circuit board; and

a plurality of packaged integrated circuits mounted on the circuit board, wherein each packaged integrated circuit includes an array of photosensors linearly aligned along a length of a surface region of an integrated circuit die therein and a plurality of conductive leads adapted for soldering to the circuit board,

wherein the packaged integrated circuits are mounted in a line on the circuit board with the surface regions of each packaged integrated circuit in alignment,

wherein at least some of the leads for each packaged integrated circuit are soldered to the circuit board, and

wherein only leads on one side of each packaged integrated circuit mounted on the circuit board are soldered to the circuit board, while leads on an other side of each packaged integrated circuit are left in floating contact with conductive structures on the circuit board to facilitate adjustment and removal of packaged integrated circuits.

17. (Previously Presented) A mold for packaging integrated circuits, comprising:

a surface against which a lead frame strip may be placed, the surface including pins projecting therefrom which are received by tooling holes within a lead frame strip placed against the surface;

a plurality of cavity regions extending from the surface, each cavity region receiving an integrated circuit die affixed to the lead frame strip and bond wires connecting the integrated circuit

die to the lead frame strip when the lead frame strip is placed against the surface, each cavity region formed by:

a first surface contacting a surface region of the integrated circuit die over a plurality of photosensors therein when the lead frame strip is placed against the surface and preventing encapsulate material from adhering to the surface region of the integrated circuit die, and

a sloped surface extending from the first surface of the cavity to form an area receiving the bond wires connecting the integrated circuit die to the lead frame strip when the lead frame strip is placed against the surface.

18. (Original) The mold of claim 17, wherein the plurality of cavity regions are contiguous and form a single cavity.